

Notice of References Cited			Application/Control No.	Applicant(s)/Patent Under Reexamination	
			09/691,083		BODEN ET AL.
Examiner		Art Unit		2823	Page 1 of 1
George Fourson					

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
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FOREIGN PATENT DOCUMENTS

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf, S., "Silicon Processing For the VLSI Era: Vol.2, Process Integration", Lattice Press, pp.207-210, 1986
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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